Aron Ahmadia, a 2007 summer research intern at IBM in the group of Dario Gil and an NSF RTG fellow at Columbia University, successfully defends his thesis proposal *Algorithms for Massively Parallel Global Optimization in Computational Lithography* before Professors Daniel Bienstock, Matias Courdurier, and David Keyes of Columbia and Dr. Alan Rosenbluth, IBM on 10 April 2008.
“Towards Virtual Fabrication”
An IBM-Columbia Workshop on Computational Lithography, Advanced Semiconductor Technology, Nanotechnology, and Supporting Disciplines

11 April 2008
9:30 – 4:30pm
School of Engineering and Applied Science
Columbia University, 500 W 120th Street, NY, NY 10027

9:30 – 9:55 Meet and greet over coffee, tea, juice & bagels

10:00 – 10:15 IBM Research and Development Overview and Opportunities

10:15 – 10:45 IBM Research and Development Overview and Opportunities

IBM Technical Presentations
10:45 – 11:15 Computational Lithography
11:15 – 11:45 Predictive Modeling (Process and Devices)
11:45 – 12:15 Algorithms & High Performance Computing

12:20 – 1:25 Lunch (sponsored by the Vice President for Research)

1:30 – 1:45 Columbia Research and Education Capabilities and Interests

Columbia Technical Presentations
1:45 – 3:15 Faculty from SEAS and GSAS

3:20 – 3:55 Informal discussions over wine & cheese

4:00 – 4:30 Summary and “Next Steps”

Presentations in CEPSR 414 “Sindeband Room” (campus entrance level to the left of Davis Auditorium, just before elevator lobby)

Food and beverages in Mudd 210, APAM Conference room (two floors down from campus level, one floor up from garage level, off the APAM Office)